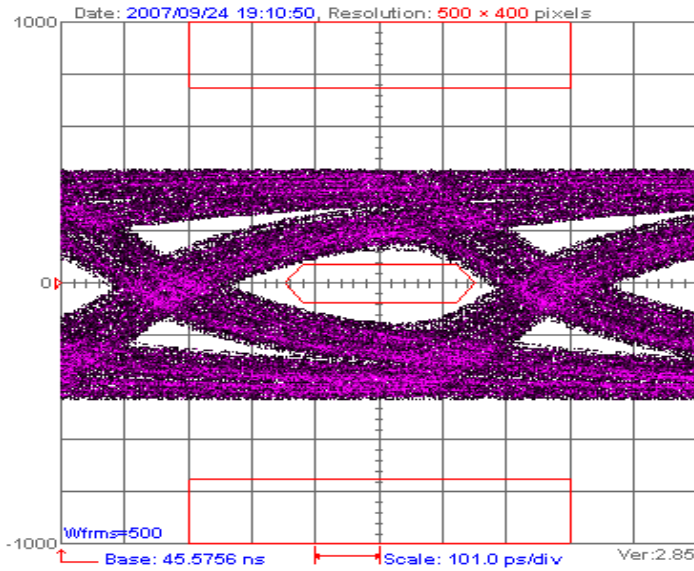
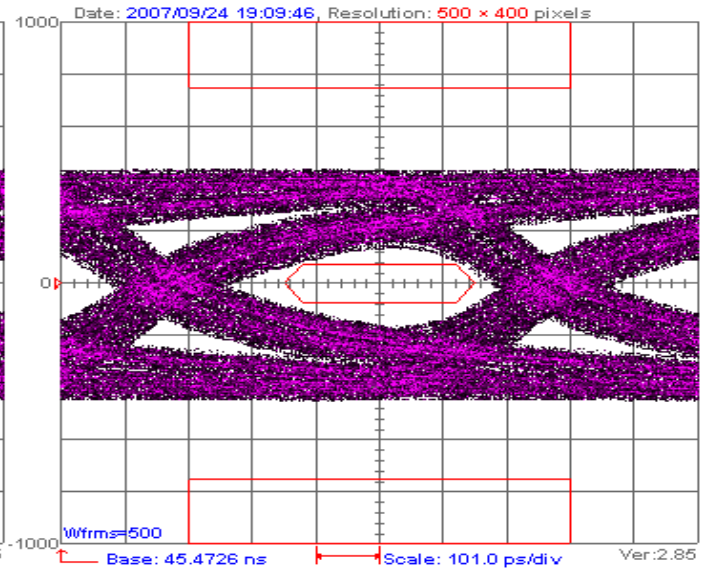




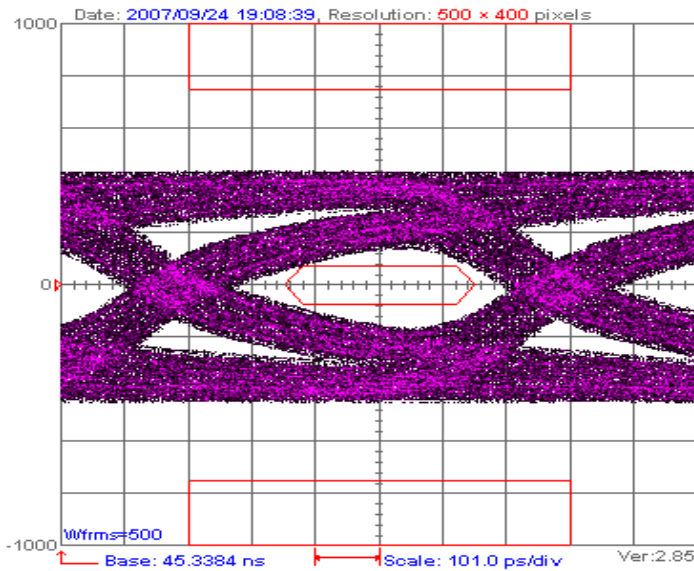
## HDMI v.1.3b CATEGORY 2 COMPLIANCE TEST 9 METER CHROMA 5<sup>2</sup>



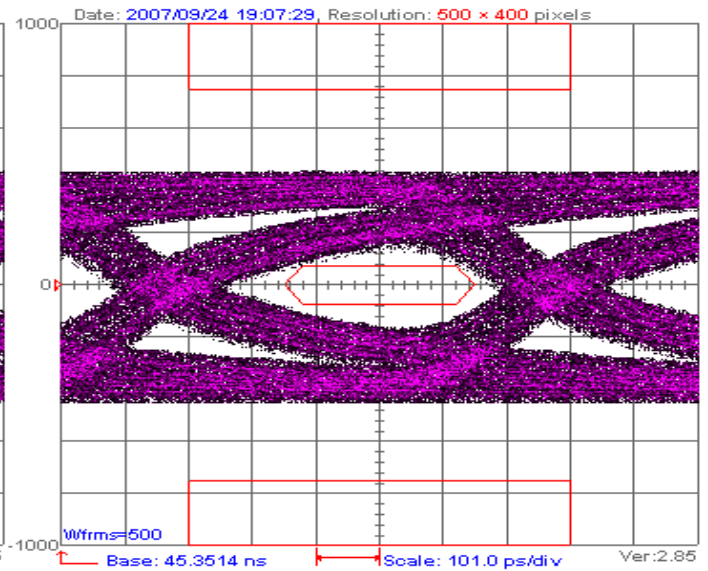
Parameter Name: HDMI Eye Diagram CLK	
Display Mode: TMDS Single Link Maximum pixel rates 165 MHz	
dRate: 1.65 Gbits/s; 606.1 ps	V_Amp: 500 mV; Offset: 0 mV
JGen: OFF	Result: Pass



Parameter Name: HDMI Eye Diagram D0	
Display Mode: TMDS Single Link Maximum pixel rates 165 MHz	
dRate: 1.65 Gbits/s; 606.1 ps	V_Amp: 500 mV; Offset: 0 mV
JGen: OFF	Result: Pass



Parameter Name: HDMI Eye Diagram D1	
Display Mode: TMDS Single Link Maximum pixel rates 165 MHz	
dRate: 1.65 Gbits/s; 606.1 ps	V_Amp: 500 mV; Offset: 0 mV
JGen: OFF	Result: Pass



Parameter Name: HDMI Eye Diagram D2	
Display Mode: TMDS Single Link Maximum pixel rates 165 MHz	
dRate: 1.65 Gbits/s; 606.1 ps	V_Amp: 500 mV; Offset: 0 mV
JGen: OFF	Result: Pass

### Test Equipment Information

ID	Product and Serial Number	Description with series number	Manufacturer
1	DTG5334(B010132)	Data Timing Generator	Tektronix
2	86100A(US41060872)	Time Domain Reflector(TDR)	Agilent(HP)